

Notice of Allowability

Application No.

09/736,354

Examiner

Anh V La

Applicant(s)

SADJADI, LEONARD

Art Unit

2636

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to the amendment filed on Jan. 25, 2005 and interview on Feb. 7, Feb. 8, and Jan. 31, 2005.
2. ☒ The allowed claim(s) is/are 19-25, 37-49 and 60-63.
3. ☒ The drawings filed on 14 December 2000 are accepted by the Examiner.
4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|---|---|
| 1. <input type="checkbox"/> Notice of References Cited (PTO-892) | 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 6. <input checked="" type="checkbox"/> Interview Summary (PTO-413), Paper No./Mail Date <u>attached herein</u> . |
| 3. <input type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date _____ | 7. <input checked="" type="checkbox"/> Examiner's Amendment/Comment |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit of Biological Material | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance |
| | 9. <input type="checkbox"/> Other _____. |

I. Statement of Reasons for Allowance:

The following is an examiner's statement of reasons for allowance:

Regarding claim 19, the prior art cited herein fails to disclose a circuit in which a current interrupter is connected to interrupt current to a load comprising an indicator, the current interrupter and the load being connected between a first node and a second node, a third node being defined between the current interrupter and the load, and a passive network comprising a first resistor connected between the first node and a fourth node, a second resistor connected between the fourth node and the second node, a third resistor connected between the second node and a fifth node, a rectifier connected between the third node and the fifth node, the indicator connected between the third node and the fifth node, wherein the first, second, and the third resistors are sized and the rectifier is aligned for current to flow in a first direction through the indicator when the current is passing through the interrupter and in a second direction when the current interrupter is interrupting current flow to the load.

Regarding claim 24, the prior art cited herein fails to disclose a circuit in which a circuit breaker is connected to interrupt current to a load comprising an indicator, the circuit breaker and the load being connected between a first node and a second node, a third node being defined between the circuit breaker and the load, and a passive network comprising a first resistor connected between the first node and a fourth node, a rectifier connected between the third node and a fifth node, a second resistor connected between the fourth node and a first one of a status output contacts of the circuit breaker, the fifth node connected to a second one of the status output contacts of the circuit breaker, a third resistor connected between a third one of the status output contacts and the second node, the third one of the status output contacts being switched between the first one and the second one of the status output contacts, and

wherein the first, second, and the third resistors are sized and the rectifier is aligned for current to flow in a first direction through the indicator when the current is passing through the circuit breaker and in a second direction when the circuit breaker is interrupting current flow to the load.

Regarding claim 37, the prior art cited herein fails to disclose an apparatus in which a circuit breaker is connected between a power supply and a load comprising an indicator, the circuit breaker and the load being connected between a first node and a second node, a third node being defined between the circuit breaker and the load, the apparatus comprising a first resistor connected between the first node and a fourth node, a second resistor connected between the third node and a fifth node, a first rectifier connected in series with the second resistor between the third node and a fifth node, the indicator connected between the fourth and the fifth node, the fourth node connected to a first one of a status output contacts of the circuit breaker, the fifth node connected to a second one of the status output contacts of the circuit breaker, a third one of the status output contacts connected to the second node, the third one of the status output contacts being switched between the first one and the second one of the status output contacts, and wherein the first and the second resistors are sized and the rectifier is aligned for current to flow in a first direction through the indicator when the current is passing through the circuit breaker and in a second direction when the circuit breaker is interrupting current flow to the load.

Regarding claim 60, the prior art cited herein fails to disclose an apparatus in which a circuit breaker is connected between a power supply and a load comprising an indicator, the circuit breaker and the load being connected between a first node and a second node, a third node being defined between the circuit breaker and the load, the apparatus comprising a first resistor connected between the first node and a fourth

node, a second resistor connected between the second node and a fifth node, a first rectifier connected between the third node and the fourth node, the indicator connected between the fourth and the fifth node, the fifth node connected to a second one of the status output contacts of the circuit breaker, a third one of the status output contacts of the circuit breaker, and wherein the first and the second resistors are sized and the rectifier is aligned for current to flow in a first direction through the indicator when the current is passing through the circuit breaker and in a second direction when the circuit breaker is interrupting current flow to the load.

II. Examiner's Amendment:

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CAR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.
2. Authorization for this examiner's amendment was given in a telephone interview with Mr. Mark W. Handley on February 08, 2005

Examiner's Amendment

In the claims:

Claims 16-18, 26-36, 50-59, and 64-66 have been canceled.

In claim 19, line 1, the phrase "(Withdrawn)" has been changed to --(Currently Amended) --.

In claim 19, line 8, the phrase "the current interrupter" has been changed to -- the current interrupter, in an AC or DC circuit, for positive or negative ground--.

In claim 20, line 2, the phrase "current interrupter" has been changed to - -circuit breaker--.

In claim 20, line 5, the phrase "current interrupter" has been changed to - -circuit breaker--.

In claim 20, line 6, the phrase "current interrupter" has been changed to - -circuit breaker--.

In claim 20, line 9, the phrase "the current interrupter" has been changed to - - the circuit breaker, in an AC or DC circuit, for positive or negative ground--.

In claim 20, line 28, the phrase "an overload;" has been changed to - -an overload; and--.

In claim 24, line 1, the phrase "(Withdrawn)" has been changed to - -(Currently Amended) --.

In claim 24, line 2, the phrase "current interrupter" has been changed to - -circuit breaker--.

In claim 24, line 4, the phrase "current interrupter" has been changed to - -circuit breaker--.

In claim 24, line 5, the phrase "current interrupter" has been changed to - -circuit breaker--.

In claim 24, line 8, the phrase "the current interrupter" has been changed to - - the circuit breaker, in an AC or DC circuit, for positive or negative ground--.

In claim 25, line 2, the phrase "current interrupter" has been changed to - -circuit breaker--.

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In claim 25, line 4, the phrase "current interrupter" has been changed to - -circuit breaker--.

In claim 25, line 5, the phrase "current interrupter" has been changed to - -circuit breaker--.

In claim 25, line 8, the phrase "the current interrupter" has been changed to - - the circuit breaker, in an AC or DC circuit, for positive or negative ground--.

In claim 43, line 1, the phrase "Claim 39" has been changed to - -Claim 38--.

In claim 44, line 2, the phrase "current interrupter is one of a switch, a fuse or a circuit breaker" has been changed to - -circuit breaker is one of a switch or a fuse--.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Anh V La whose telephone number is (571) 272-2970. The examiner can normally be reached on Mon-Fri from 9:30am to 6:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jeffery Hofsass can be reached on (571) 272-2981. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



ANH V. LA
PRIMARY EXAMINER

Anh V La
Primary Examiner
Art Unit 2636

AI
February 09, 2005